
FOREWORD

Special Section on Analog Circuits and Related SoC Integration Technologies

I am honored to announce the publication of this special section on analog circuits and related SoC integration technologies. Analog electronic technology covers a very wide field of interests: RF, baseband, power electronic circuits and systems, and also semiconductor devices, modeling, CAD, and measurement technology. Analog research and design is very exciting: researchers and scientists are continuously developing new and better ideas. Also companies are recognizing the increasing importance of analog technology in the electronics industry: better technology can differentiate their products from those of other companies — and analog electronics is a key technology, so the market and the industry are always clamoring for new breakthroughs. I believe that the publication of this special section will contribute to sharing new ideas and knowledge among industry people and academia, and will promote further analog-related activities.

We have invited papers from two researchers who are famous worldwide: Prof. Arthur van Roermund Group of Eindhoven University of Technology, in the Netherlands, and Mr. Satoshi Tanaka of Renesas Technology Corp., Japan. Thirty-one regular paper manuscripts were submitted. These were reviewed and objectively judged by a panel of experts, and we have selected twelve manuscripts for publication as full papers, and two for publication as letters.

During our editorial work, Dr. Tetsuya Iida passed away. He was widely recognized as one of the most influential people working in this area; he contributed significantly to analog electronic technology and to the industry while serving as a design scientist and general manager, and he also served as the Guest Editor-in-Chief of the analog special section in 2007. Our editorial committee would like to dedicate this special section to the memory of Dr. Iida.

Finally, on behalf of the editorial committee, I would like to express our sincere thanks to all who submitted manuscripts for this special section, and to all the reviewers. I would like to thank all of the editorial committee members, as listed below, for their enthusiastic support of the editorial work.

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Haruo Kobayashi, Guest Editor-in-Chief

Haruo Kobayashi (*Member*) received B.S. and M.S. degrees in information physics from the University of Tokyo in 1980 and 1982 respectively, an M.S. degree in electrical engineering from the University of California at Los Angeles (UCLA) in 1989, and a Dr.Eng. degree in electrical engineering from Waseda University in 1995. After joining Yokogawa Electric Corp. Tokyo, Japan, in 1982, he was engaged in research and development related to measuring instruments and mini-supercomputers. From 1994 to 1997, he was involved in research and development into ultra-high-speed ADCs/DACs at Teratec Corp. He was also an adjunct lecturer at Waseda University from 1994 to 1997. In 1997 he joined Gunma University, and is presently a Professor in Electronic Engineering Department there. His research interests include mixed-signal integrated circuit design and signal processing algorithms. He received the Yokoyama Award in Science and Technology in 2003.

