FOREWORD

Special Section on Parallel and Distributed Computing and Networking

Parallel computing, distributed computing, and networking are closely-related areas of computing, whose importance are ever-increasing in these decades. The objective of this special section is to publish the recent progresses in the interdisciplinary area of parallel computing, distributed computing, and networking. We also expected that several authors would submit the extended versions of their works presented in the first International Conference on Networking and Computing (ICNC 2010), held in Hiroshima during 17-19 November, 2010, in addition to bland-new contributions from other IEICE members.

In response to our call-for-papers, a total of 32 papers and 6 letters were submitted to the editorial committee from 10 different countries and areas across the world; e.g., Japan, China, Korea, Serbia, India, Thailand, Brazil, Ireland, Italy, and Taiwan. Each manuscript was carefully reviewed, and 10 papers and 1 letter were accepted for publication. It was an extremely difficult task to select papers to be published from excellent and interesting submissions. Regrettably, we had to reject some of the interesting papers, since it is the editorial policy of IEICE transactions to reject a manuscript that requires a major revision. I also would like to note that some of the promising manuscripts were withdrawn by the authors after the first review. I hope I could see the revised versions of these manuscripts in the future issues of the IEICE transactions.

It was my honor to serve as the guest editor-in-chief of this special section. I would like to thank all contributors who submitted their high-quality papers to this special section. I also wish to express my appreciation for the efforts and contributions of the anonymous reviewers and the guest associate editors of this special section. Especially, I am most grateful to two guest editors, Dr. Shigeaki Tagashira and Dr. Hirotsugu Kakugawa, for their devotion to this special section.

Special Section Editorial Committee
Guest Associate Editors:
    Kento Aida (National Institute of Informatics), Yutaka Arakawa (Kyushu University), Yoshiaki Katayama (Nagoya Institute of Technology), Teruaki Kitasuka (Kumamoto University), Keiji Kimura (Waseda University), Michihiro Koibuchi (National Institute of Informatics), Masahiro Sasabe (Osaka University), Yuichiro Shibata (Nagasaki University), Ryoichi Shinkuma (Kyoto University), Tomoaki Tsumura (Nagoya Institute of Technology), Satoshi Fujita (Hiroshima University), Teruo Matsuzawa (Japan Advanced Institute of Science and Technology), Susumu Matsumae (Saga University), Hiroaki Morino (Shibaura Institute of Technology), Toshihiro Yamauchi (Okayama University), Hirozumi Yamaguchi (Osaka University), Takashi Yokota (Utsunomiya University)
Guest Editors:
    Hirotsugu Kakugawa (Osaka University), Shigeaki Tagashira (Kyushu University)

Shuichi Ichikawa, Guest Editor-in-Chief

Shuichi Ichikawa (*Senior Member*)   received his D.S. degree in Information Science from the University of Tokyo in 1991. He had been affiliated with Mitsubishi Electric Corporation, Nagoya University, and Toyohashi University of Technology. Currently, he is a professor of Numazu National College of Technology and a visiting professor of Toyohashi University of Technology. His research interests include parallel processing, high-performance computing, custom computing machinery, and information security. He is a member of IEEE, ACM, and IPSJ.