FOREWORD

Special Section on Parallel and Distributed Computing and Networking

Recent rapid advance of multi-core and many-core architectures has made parallel computers commonly used basis of computation. A smart phone with a dual-core processor activates the search engine working on large cloud servers through the wireless broadband network. Parallel computing, distributed computing and networking are closely-related areas of computing and their importance is ever-increasing in these decades. The objective of this special section is to publish the recent progresses in the interdisciplinary areas. We also expected that some authors would submit the extended versions of their papers presented in the second International Conference on Networking and Computing (ICNC2010), held in Osaka during November 30 to December 2, 2011, in addition to bland-new contributions from other IEICE members.

A total of 39 papers and 8 letters were submitted from 9 different countries and areas across the world; e.g., Japan, China, Korea, Malaysia, Taiwan, Mexico, Sweden, Thailand and Vietnam. We are pleased to report that the number of papers from foreign countries were more than those from Japan. Each manuscript was carefully reviewed, and 20 papers and 5 letters were accepted for publication. We had to reject some of the interesting papers, since it is the editorial policy of IEICE transactions to reject a manuscript that requires a major revision. I hope I could see the revised versions of these manuscripts in the future issues of the IEICE transactions.

It is my honor to serve as the guest editor-in-chief of this special section. I would like to thank all contributors who submitted their high-quality papers. Also, I wish to express my gratitude to the efforts and contributions of the reviewers and the guest associate editors. Finally, I am most grateful to guest editors, Dr. Tsutomu Yoshinaga and Dr. Koji Nakano, for their devotion to this special section.

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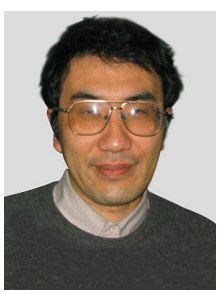

Hideharu Amano *(Member)* receievd the Ph.D.degree from the Dept. of Electronic Engineering from Keio University, Japan in 1986. He is currently a professor in the Dept. of Information and Computer Science, Keio University. His research interests include parallel architectures and reconfigurable systems.