FOREWORD

Special Section on Reconfigurable Systems

The research field on Reconfigurable Systems has become important and popular along with rapid advances of field programmable devices. A large part of ASICs has been replaced with such field programmable devices, and their application is stretched to high performance computing as well as traditional embedded applications. Partial and dynamic reconfiguration have become a common design choice rather than challenging techniques. Also, coarse grained reconfigurable architectures have been embedded into real consumer devices as a flexible low power accelerators. Important techniques on Reconfigurable Systems are widely spread to application, system software, design tools, computer architecture and device technology.

We planned a special section in order to sum up the recent technical trend of various area of Reconfigurable Systems. In response to the call for papers for this special section, 21 regular papers and four letter papers were submitted. Through the same review and editorial process as the regular section, 14 papers and one letter were accepted for publication. The selected papers will give readers the latest results of researches on various fields on Reconfigurable Systems.

The Special Section Editorial Committee Members listed below wish to thank all of those who submitted papers, as well as the reviewers for their thoughtful comments and suggestions. As the Guest Editor, I would like to express my sincere thanks to the Editorial Committee Members for their efforts to maintain the quality of the selected papers high.

The Special Section Editorial Committee Members:
Tomonori Izumi (Ritsumeikan University), *Guest Editor*, Yasunori Osana (University of Ryukyus), *Guest Editor*, Tetsuo Hironaka (Hiroshima City University), Yohei Hori (National Institute of Advanced Industrial Science and Technology), Yukihiro Iguchi (Meiji University), Masahiro Iida (Kumamoto University), Yasushi Inoguchi (Japan Advanced Institute of Science and Technology), Yoshifumi Kawamura (Renesas Electronics Corp.), Shorin Kyo (Renesas Electronics Corp.), Tsutomu Maruyama (University of Tsukuba), Yukio Mitsuyama (Kochi University of Technology), Akira Nagoya (Okayama University), Kentaro Sano (Tohoku University), Yuichiro Shibata (Nagasaki University), Minoru Watanabe (Shizuoka University), Yutaka Yamada (Toshiba Corp.), Takashi Yokota (Utsunomiya University)

Hideharu Amano, Guest Editor-in-Chief

Hideharu Amano *(Member)* received the Ph.D. degree from Keio University, Japan in 1986. He is now a Professor in the Department of Information and Computer Science, Keio University. His research interests include the area of parallel architectures and reconfigurable computing.